

Fig. 1a

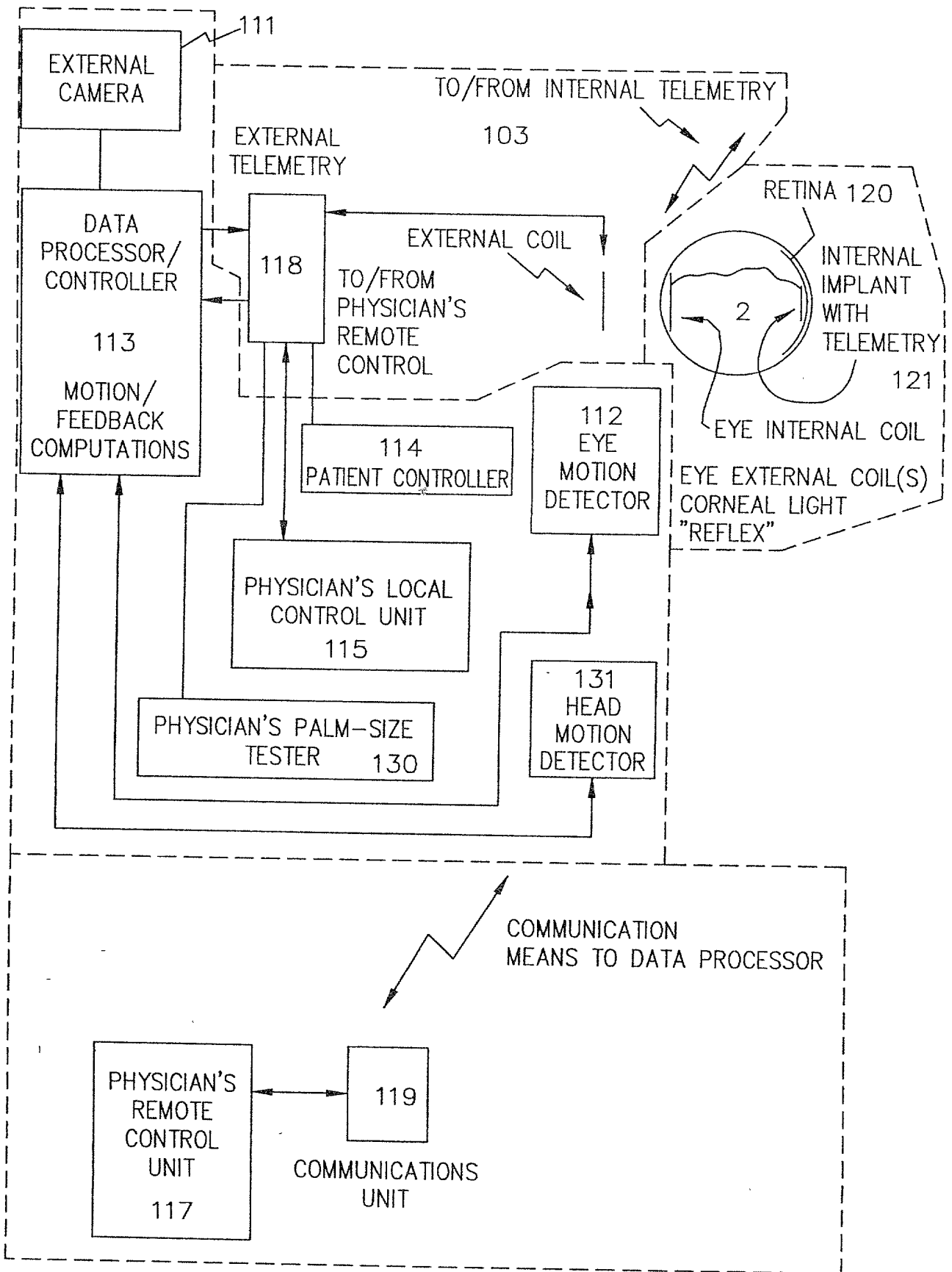


FIGURE 1b

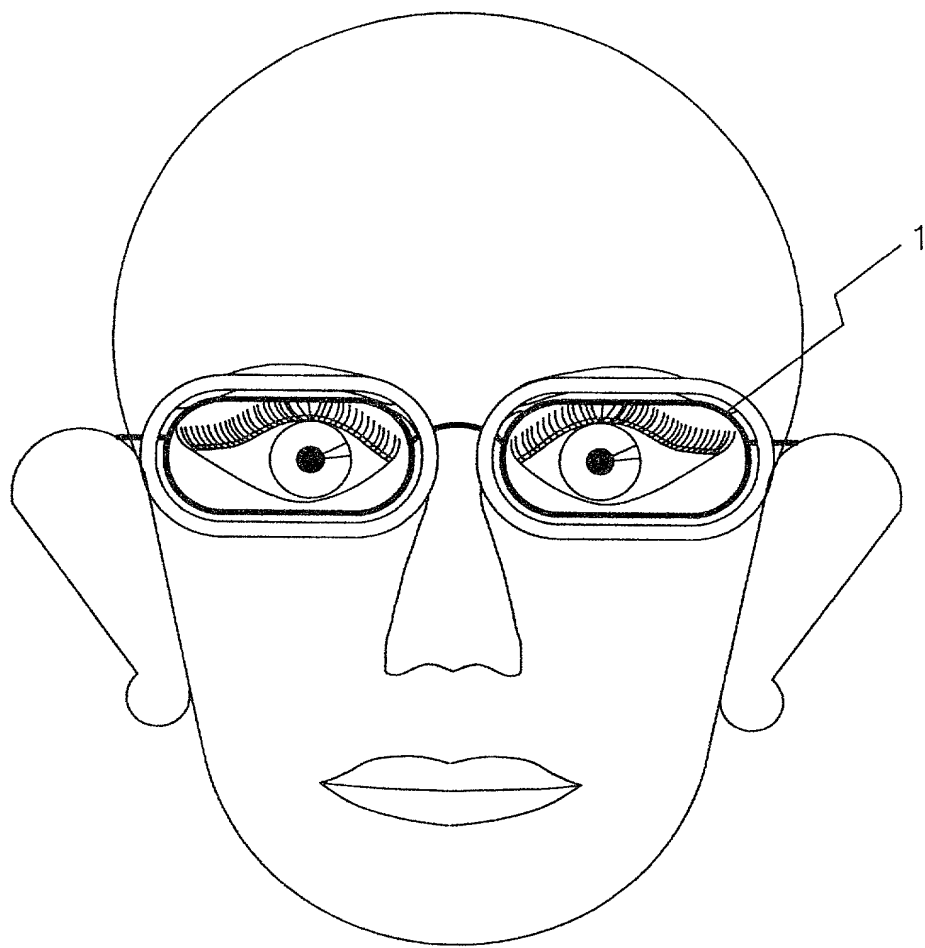


Fig. 1c

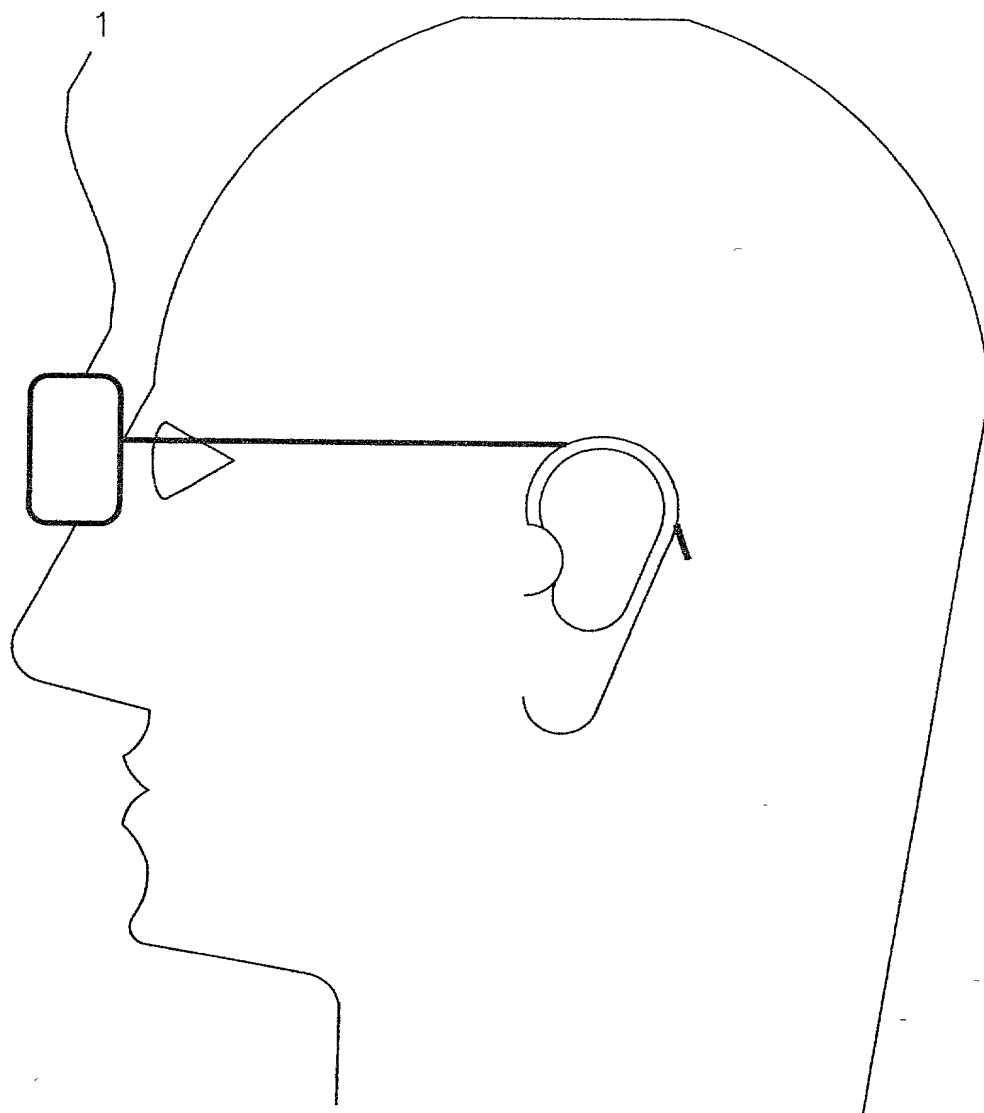


Fig 1d

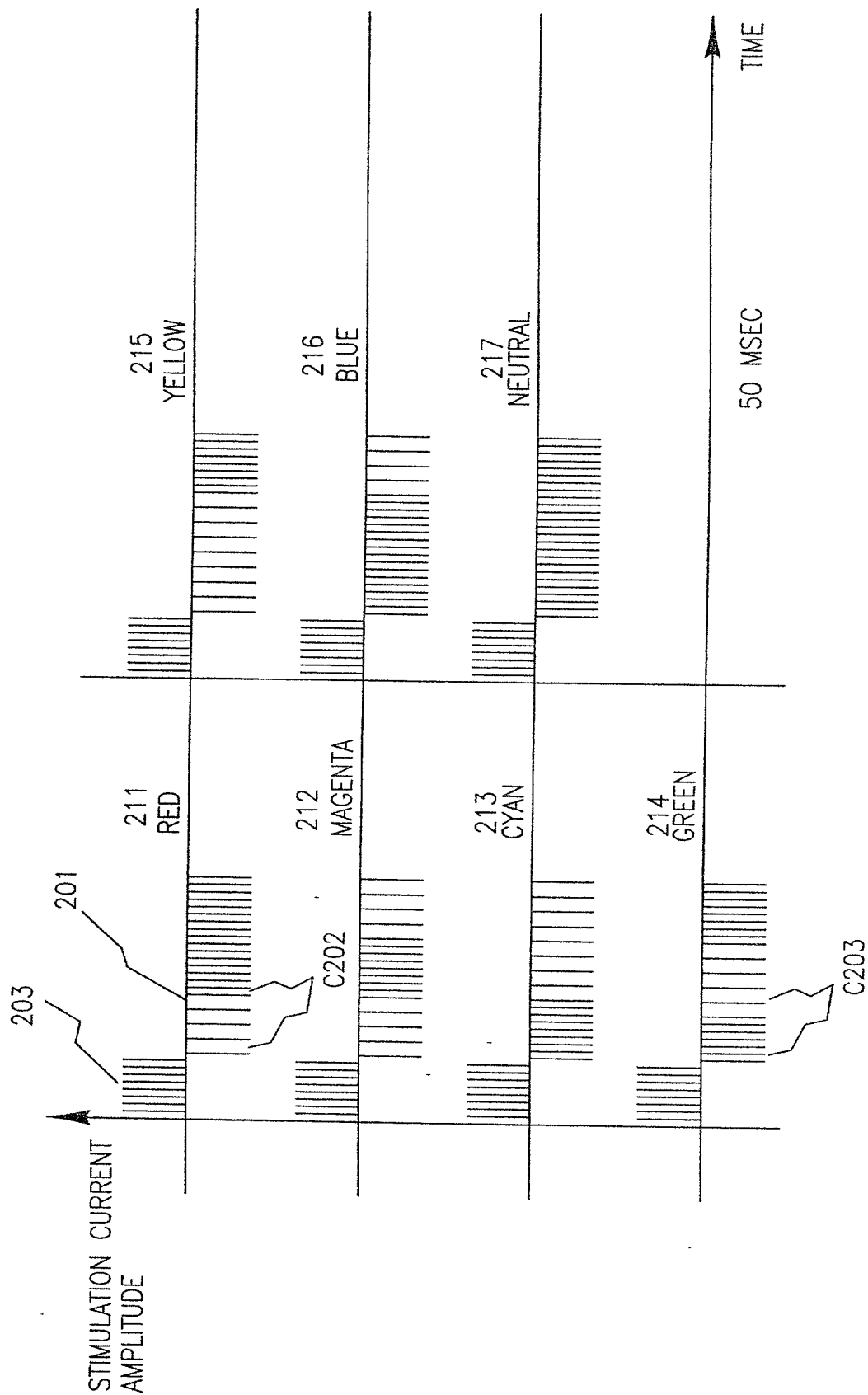


FIGURE 2a
COLOR CODING

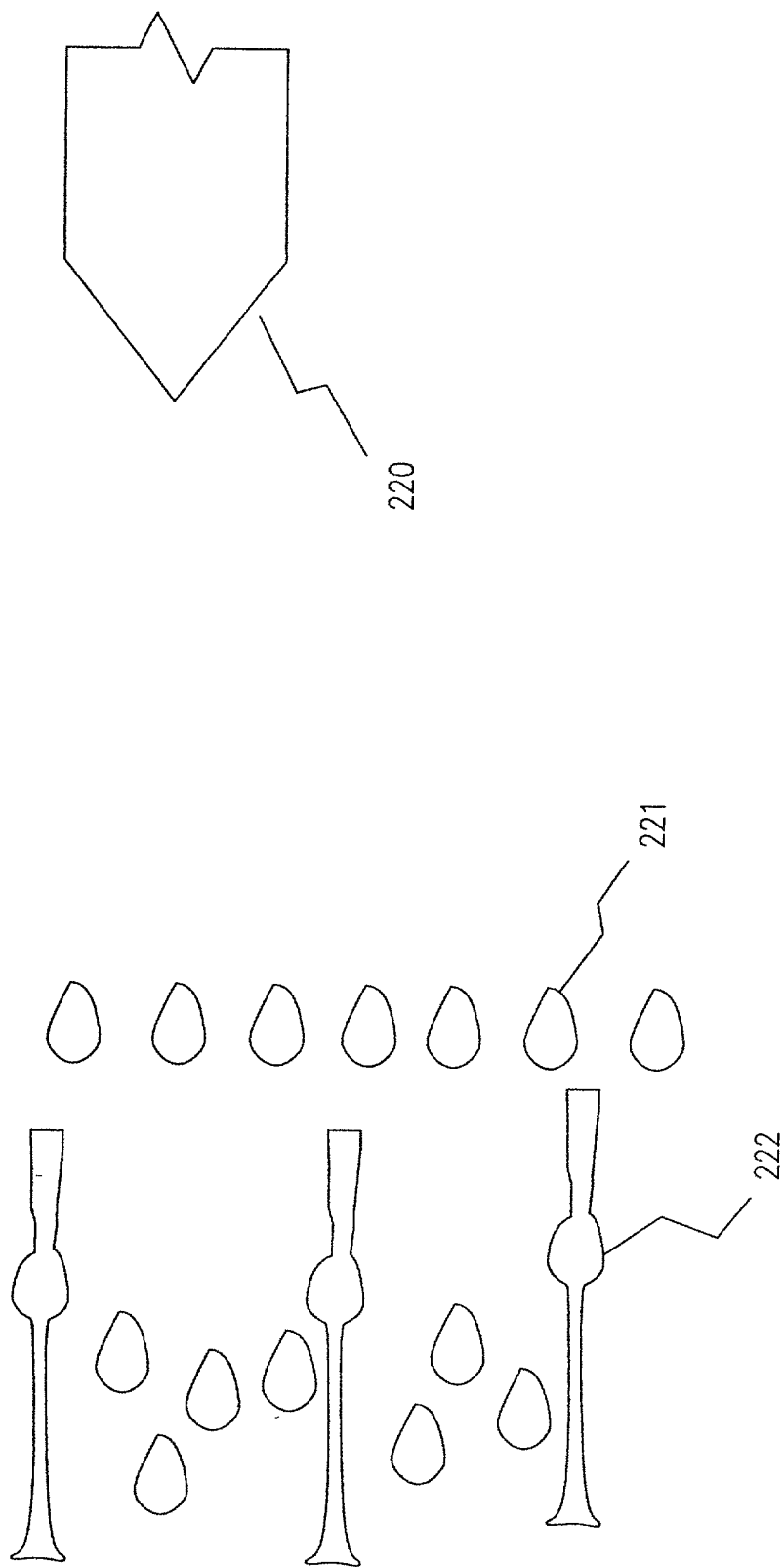


FIGURE 2b

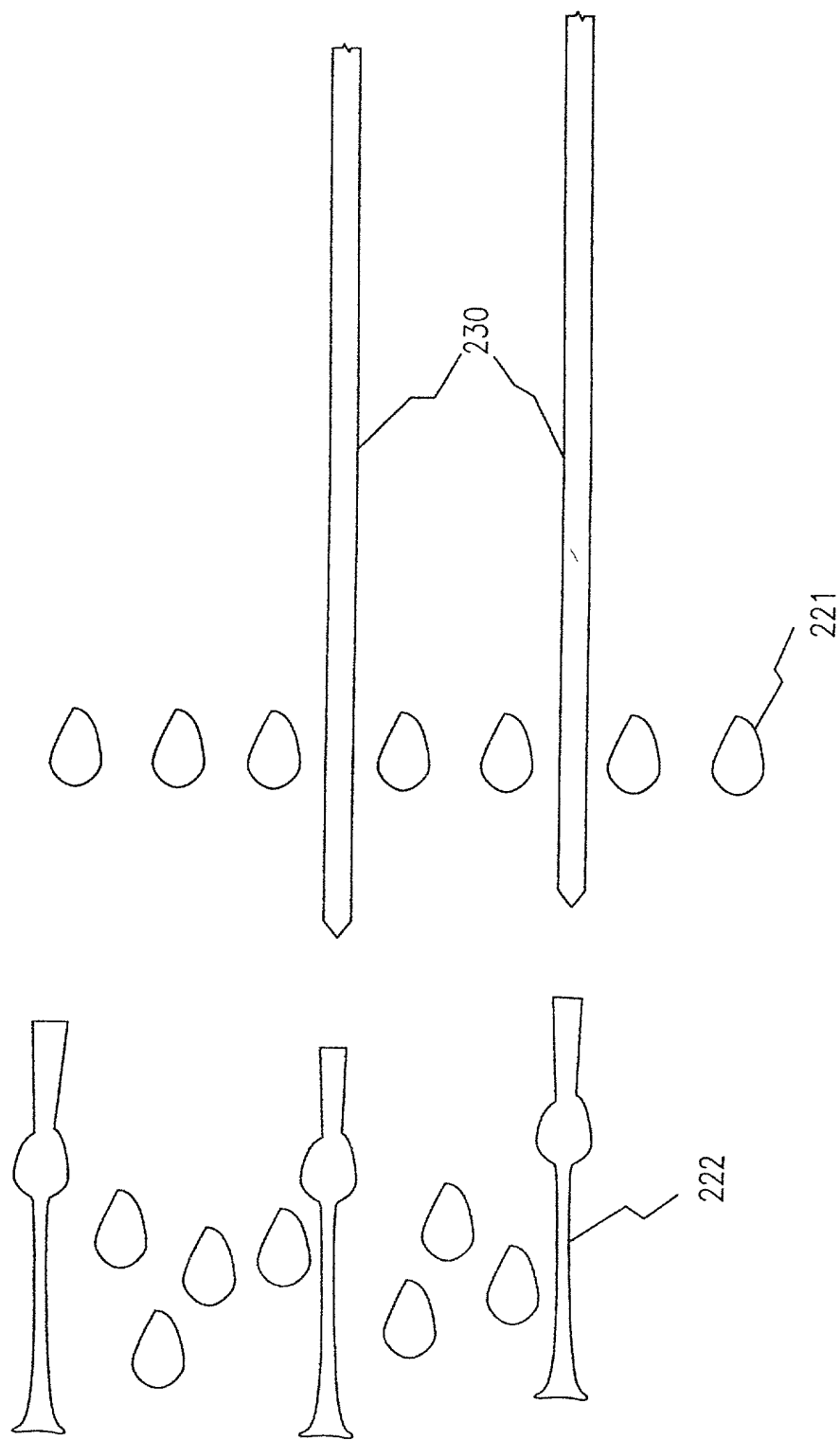
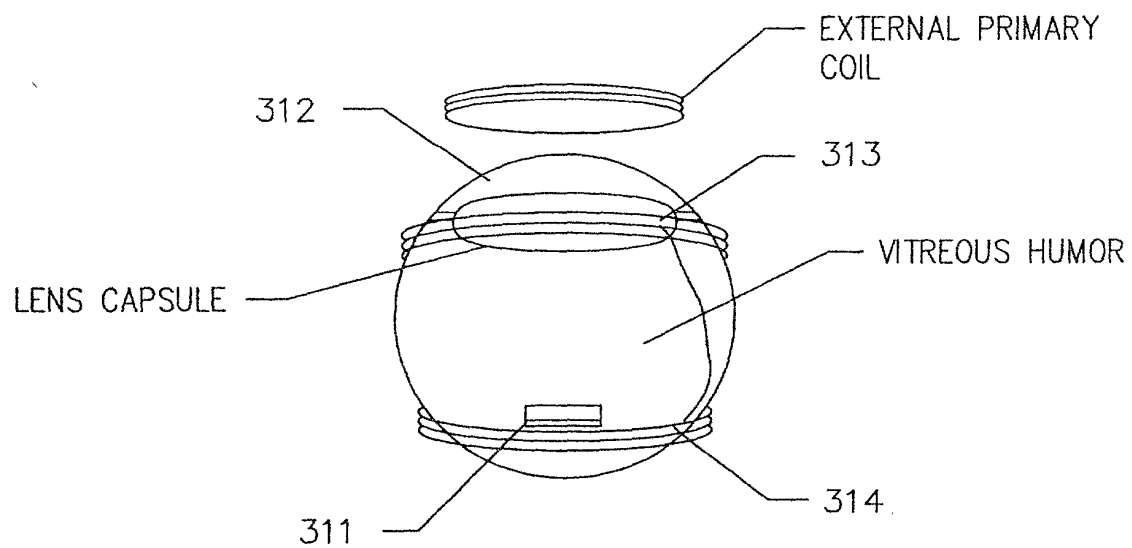


FIGURE 2c



EXTERNAL/INTERNAL TELEMETRY UNIT -
EXTERNAL COIL/INTERNAL COIL /INTERNAL CHIP

FIGURE 3a

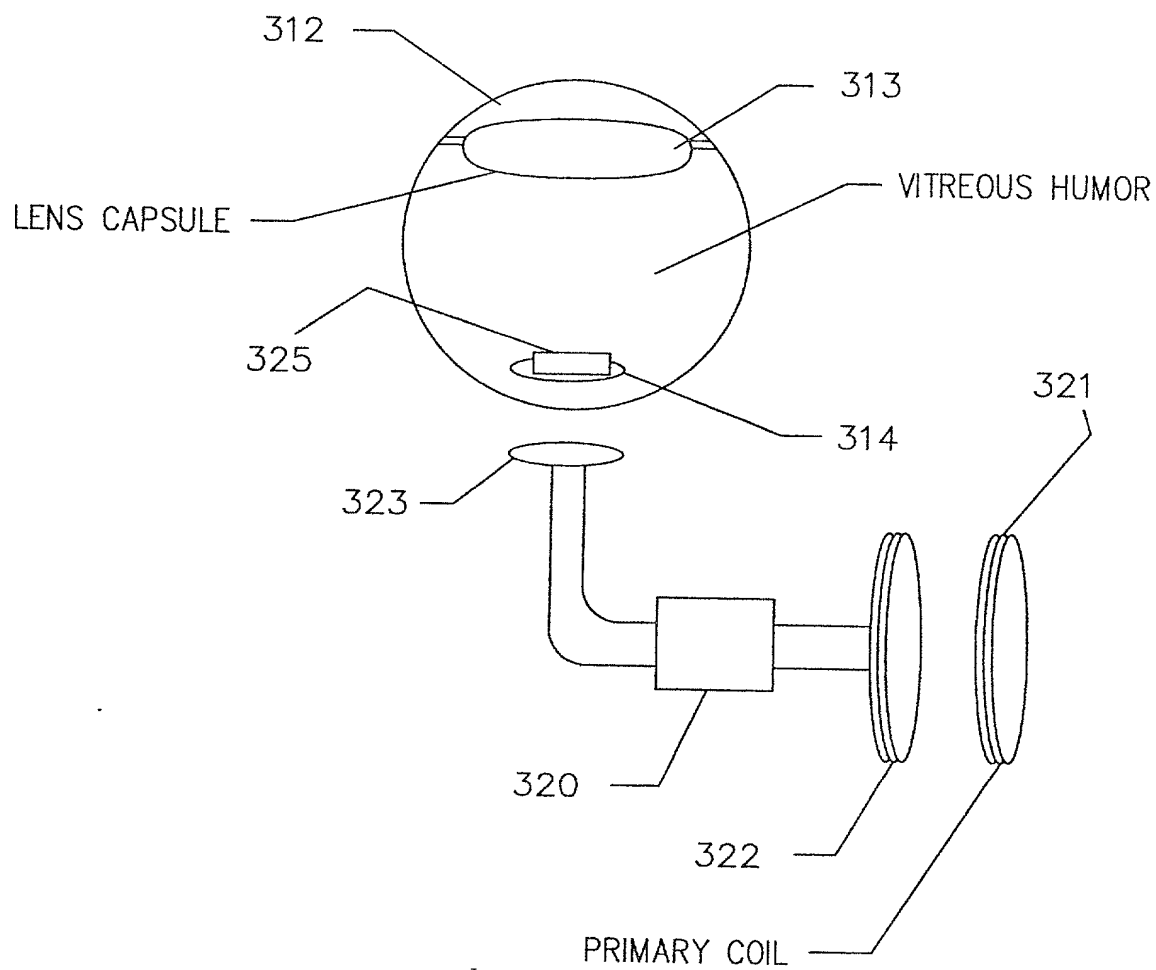


FIGURE 3b

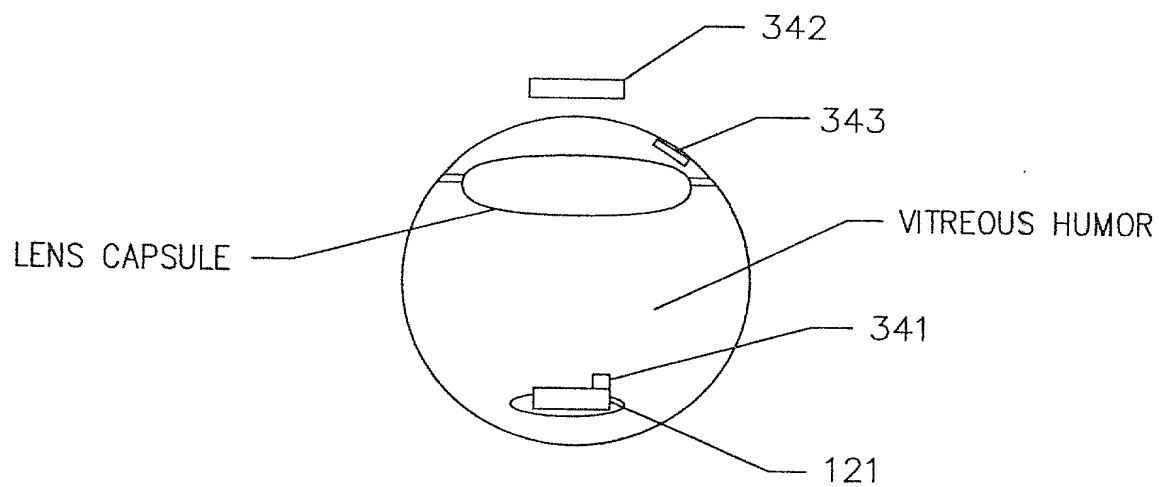


FIGURE 3c

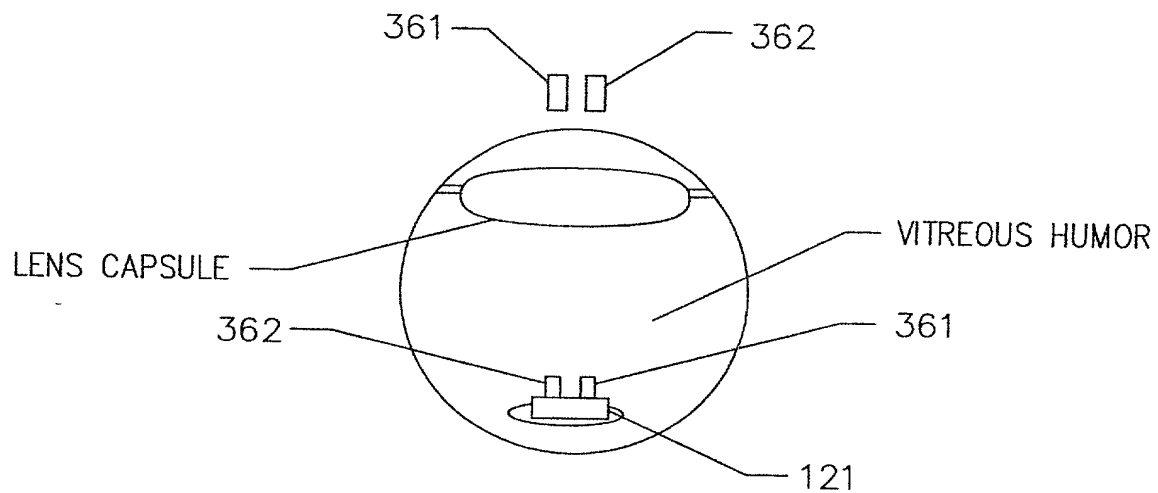
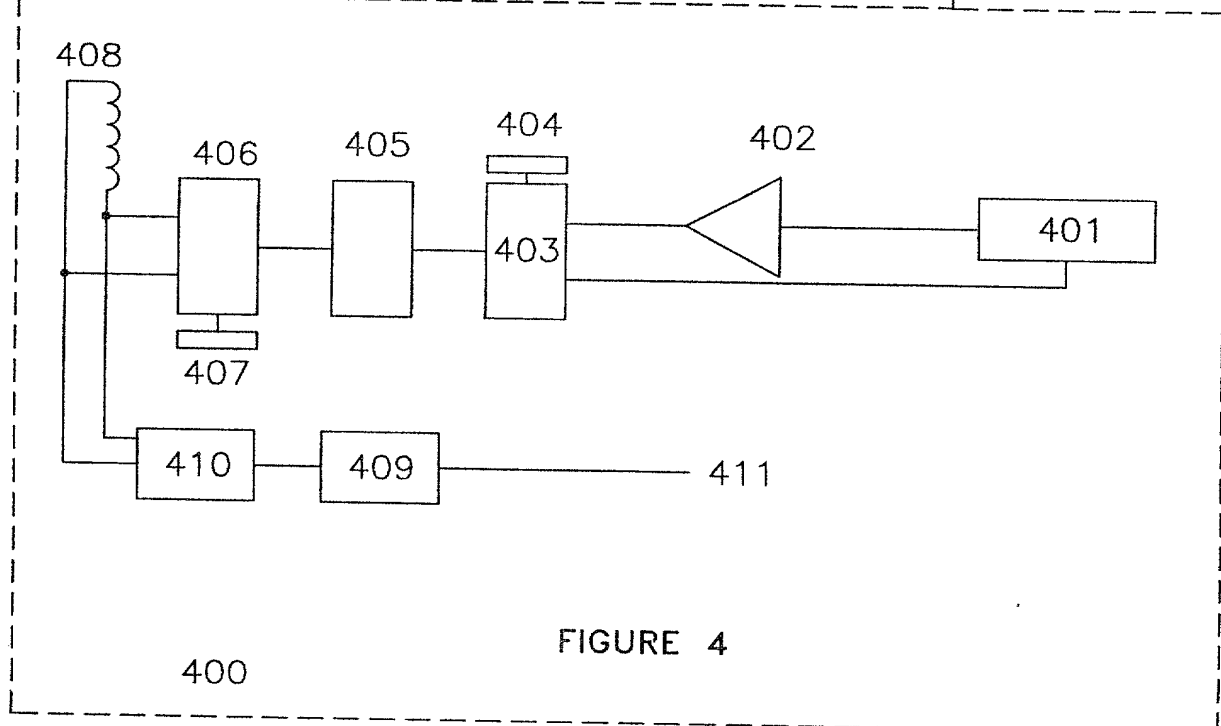
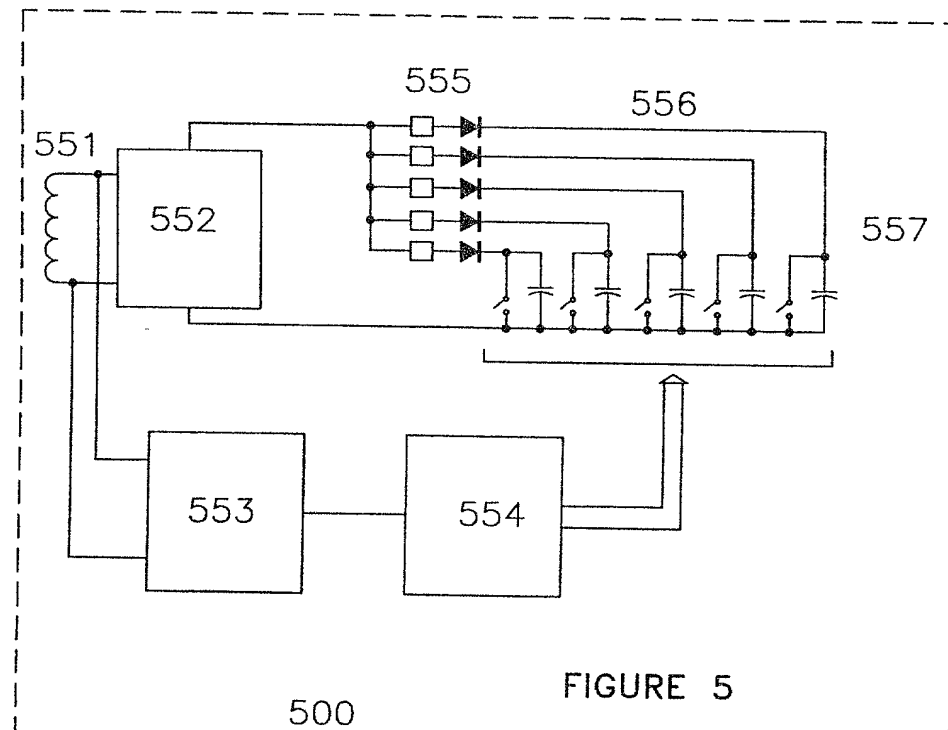
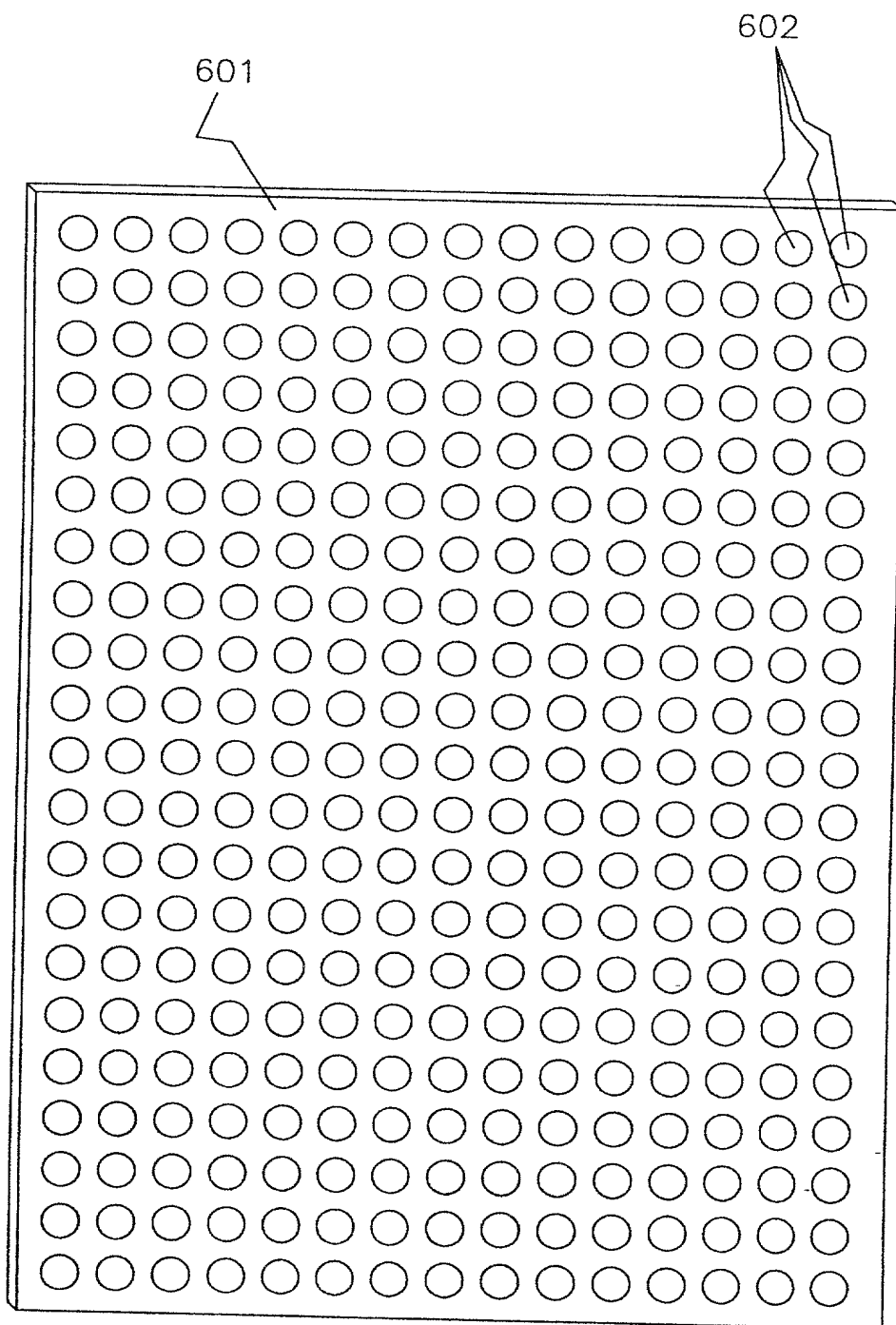


FIGURE 3d





ELECTRODES ON SUBSTRATE

FIGURE 6a

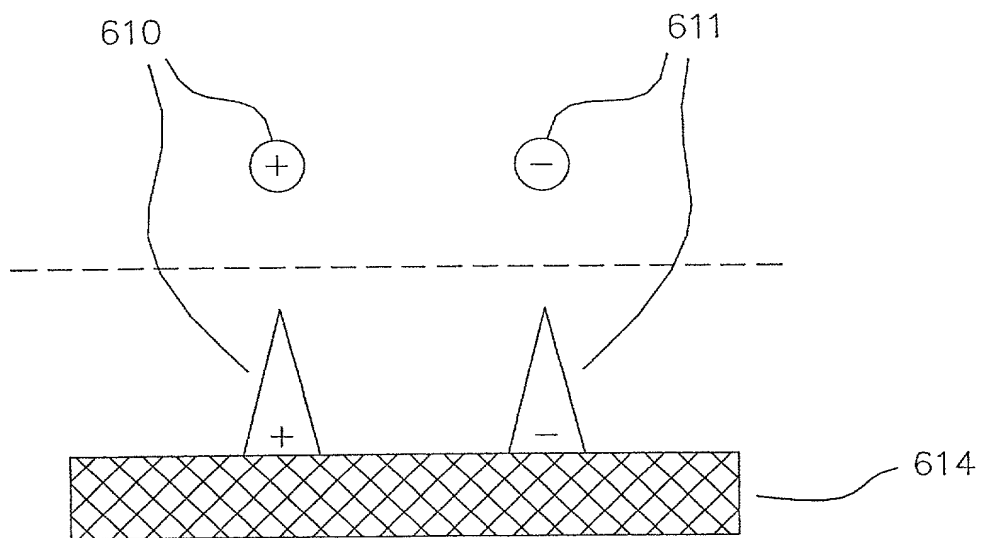


FIGURE 6b

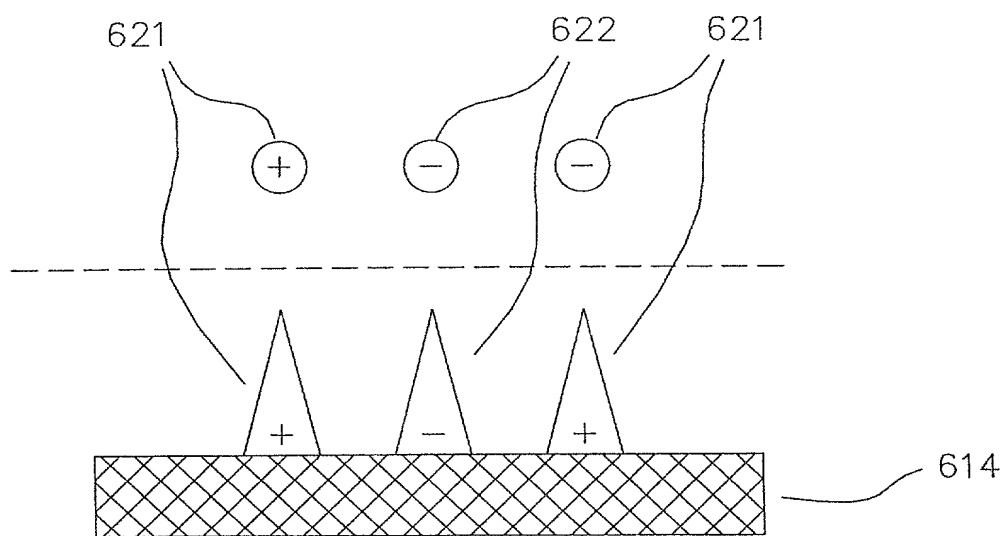
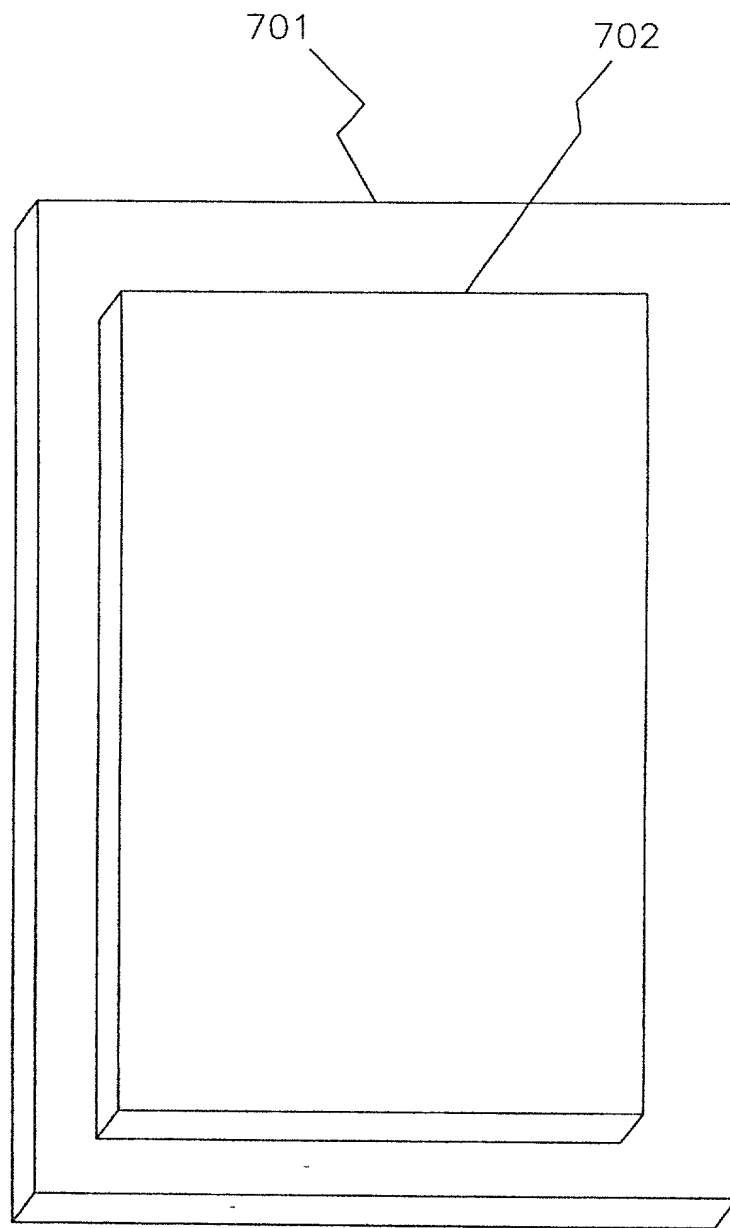


FIGURE 6c



INDIFFERENT ELECTRODE OF MONOPOLAR ELECTRODES

FIGURE 7

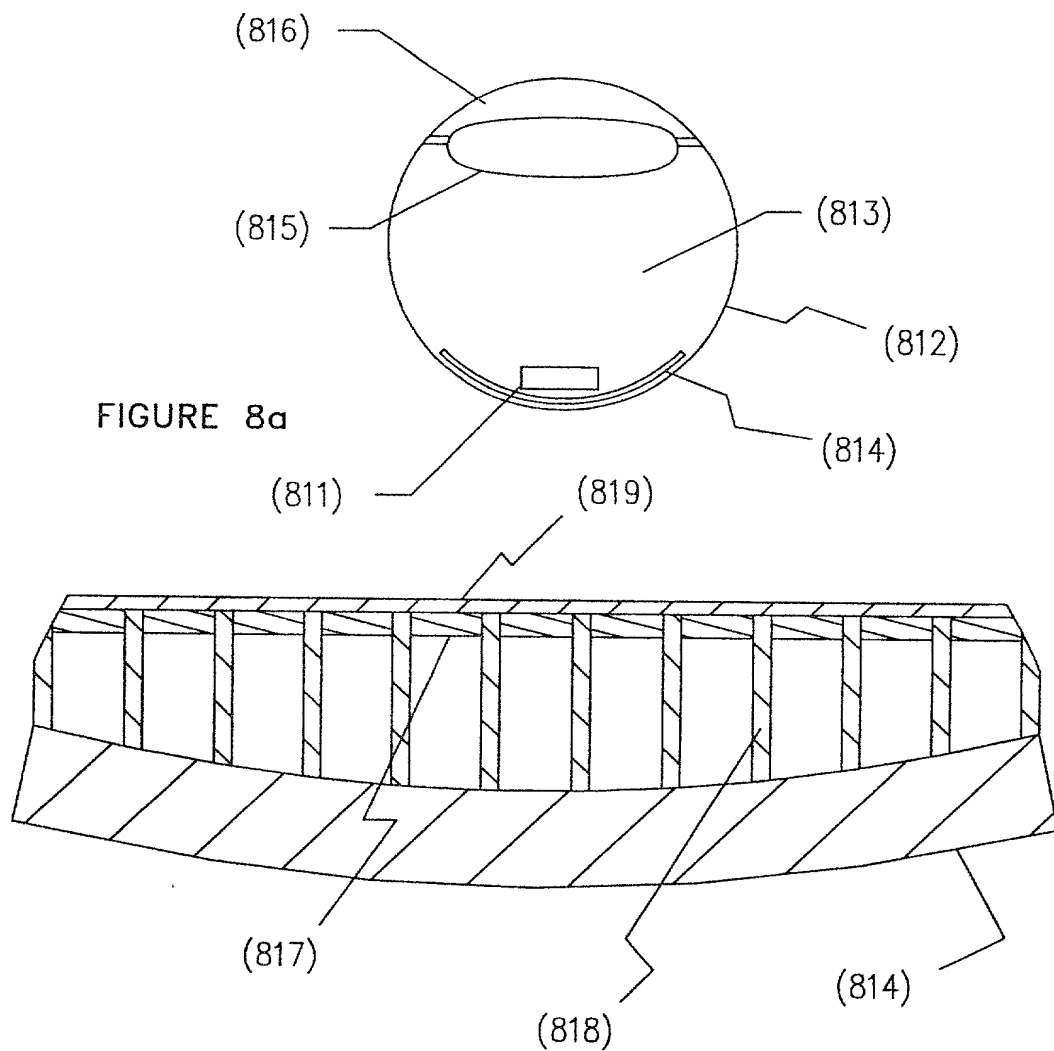


FIGURE 8b
EPIRETINAL ELECTRODES - RECESSED

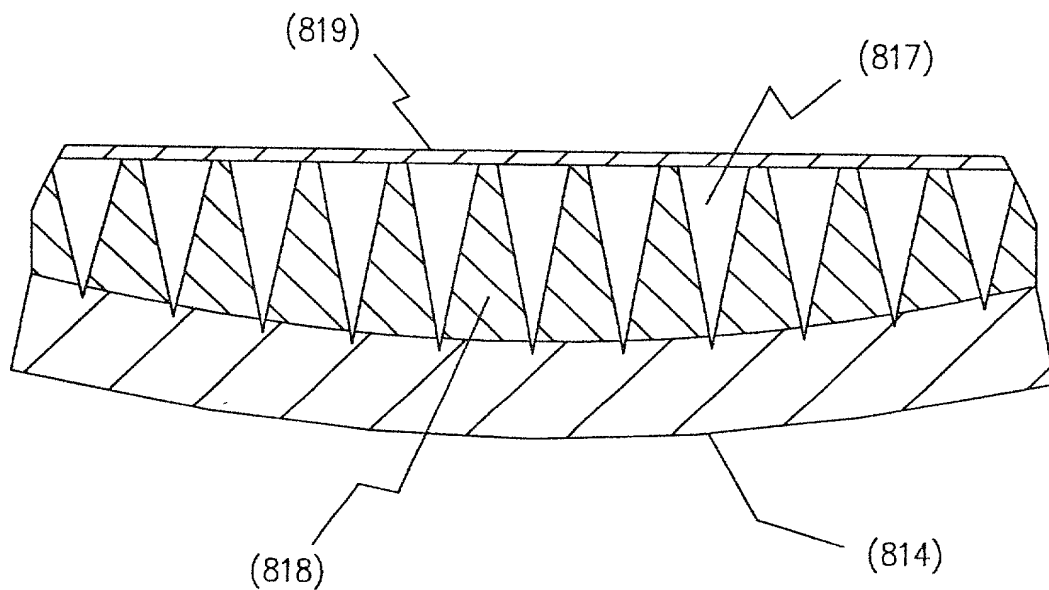


FIGURE 8c
EPIRETINAL ELECTRODES - PENETRATING

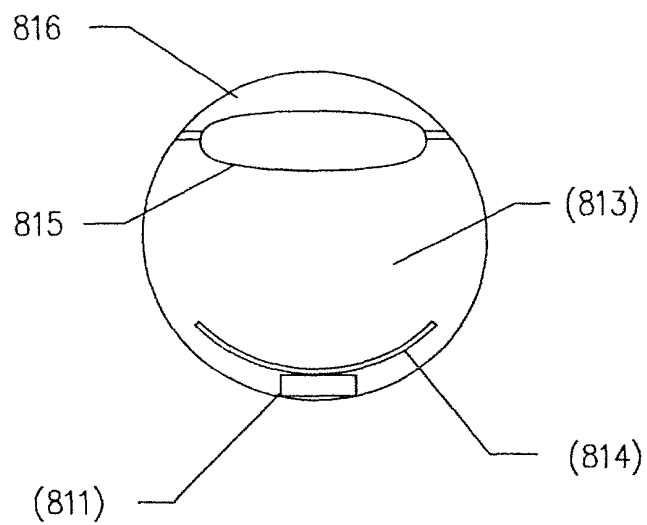


FIGURE 9a

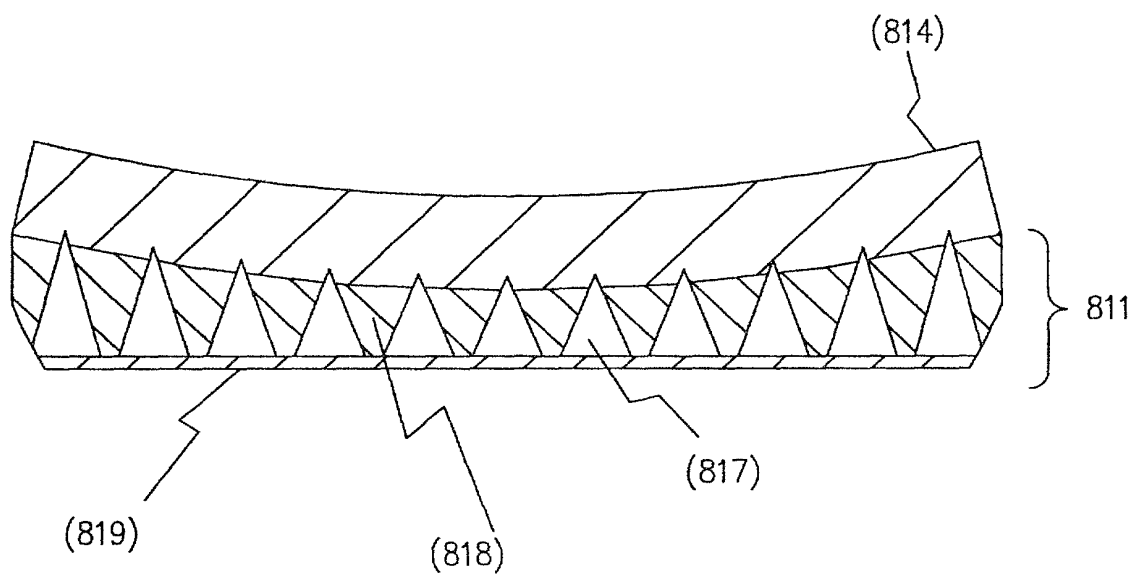


FIGURE 9b
SUBRETINAL ELECTRODES — PENETRATING

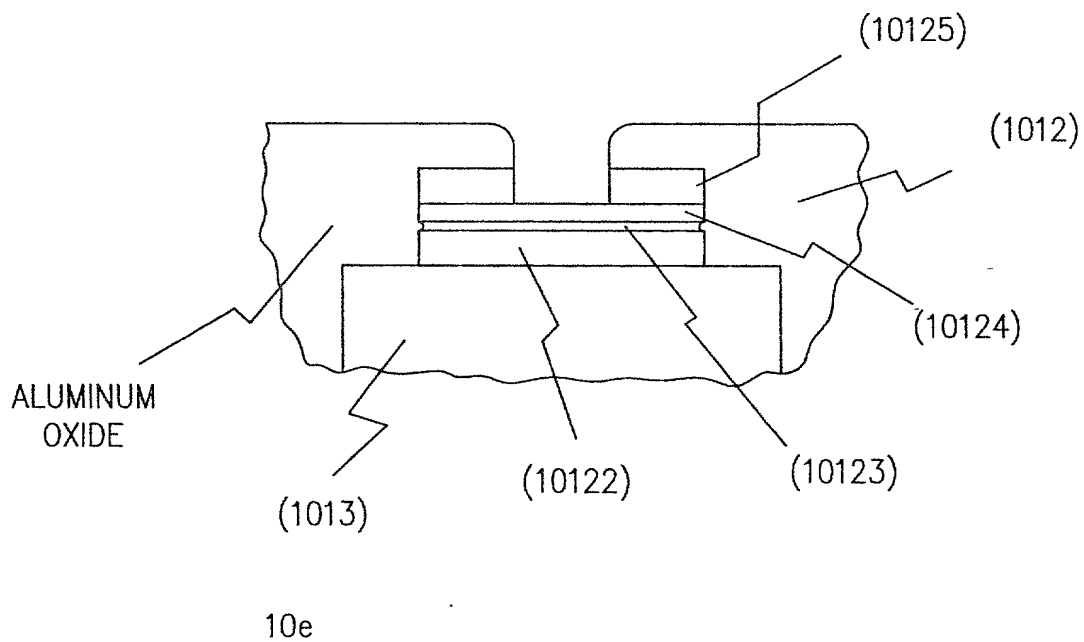
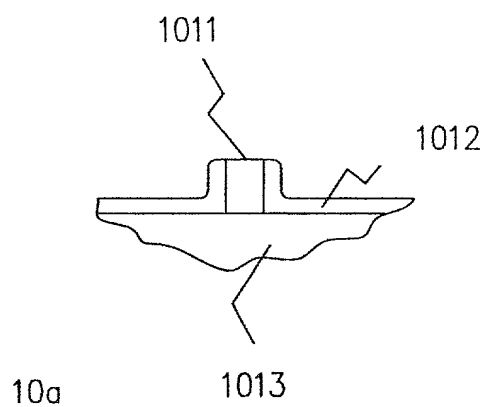
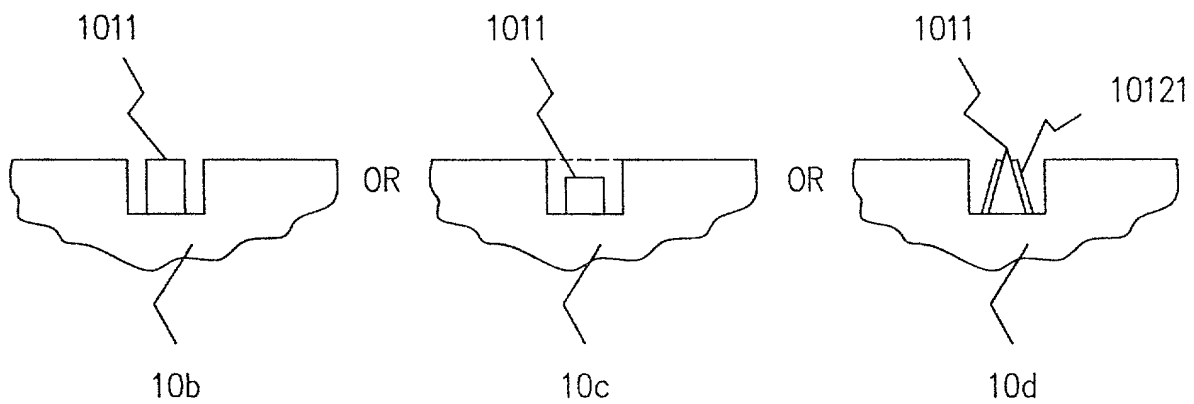


FIGURE 10

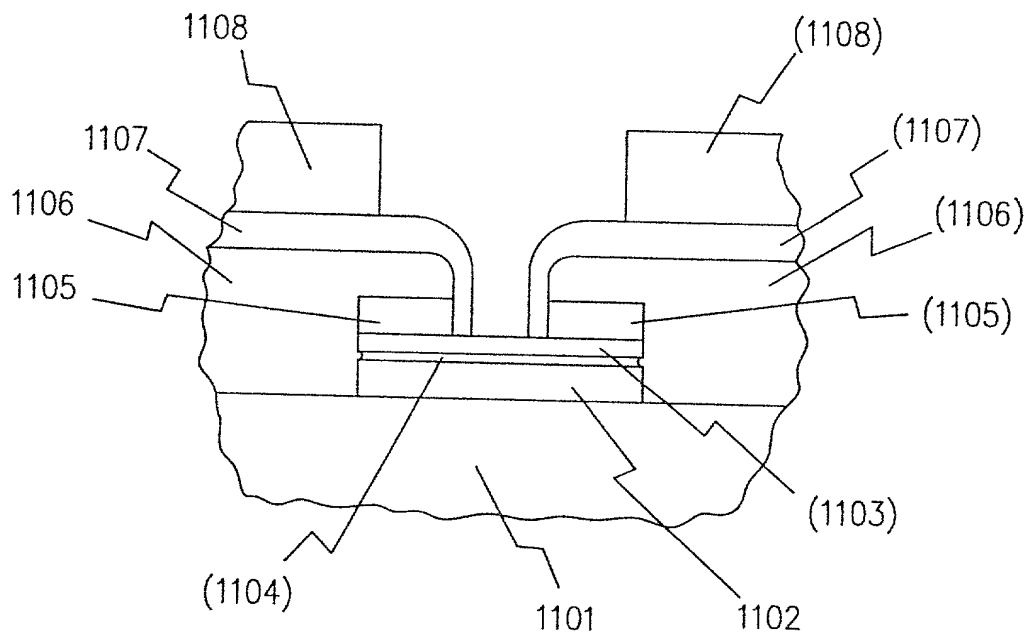


FIGURE 11a
ELECTRODES

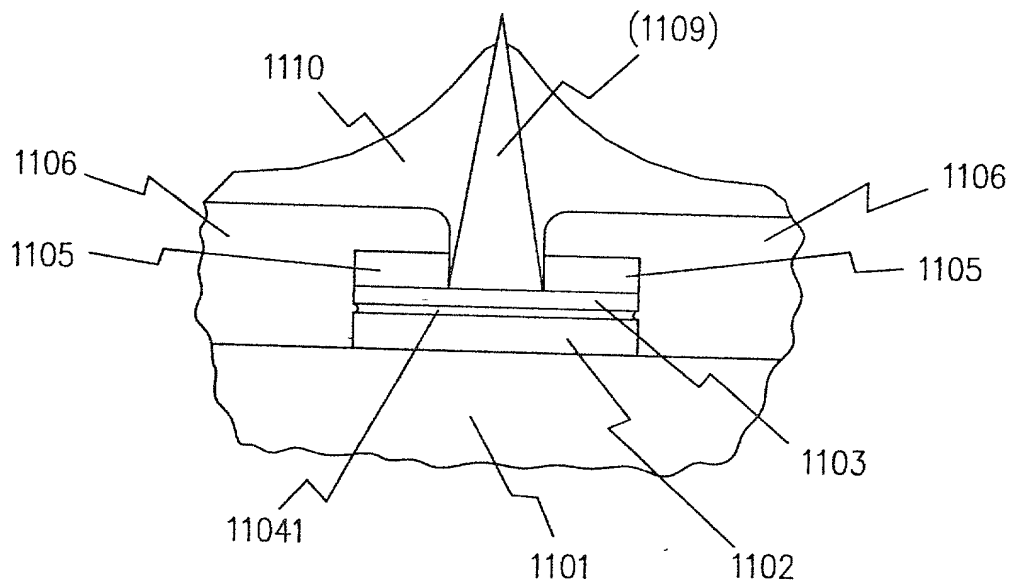


FIGURE 11b
ELECTRODES

FIGURE 11

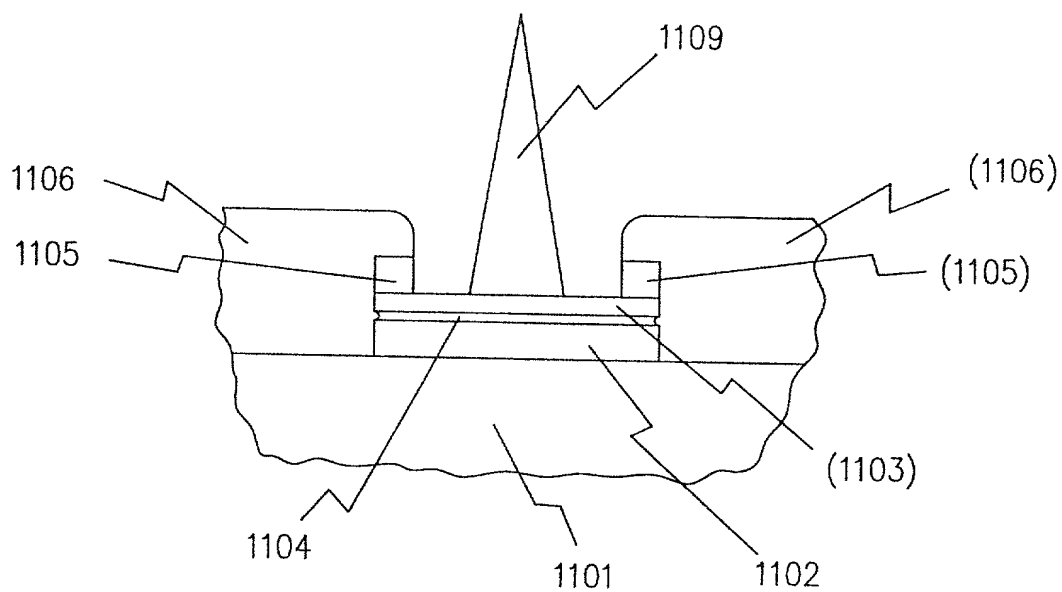


FIGURE 11c

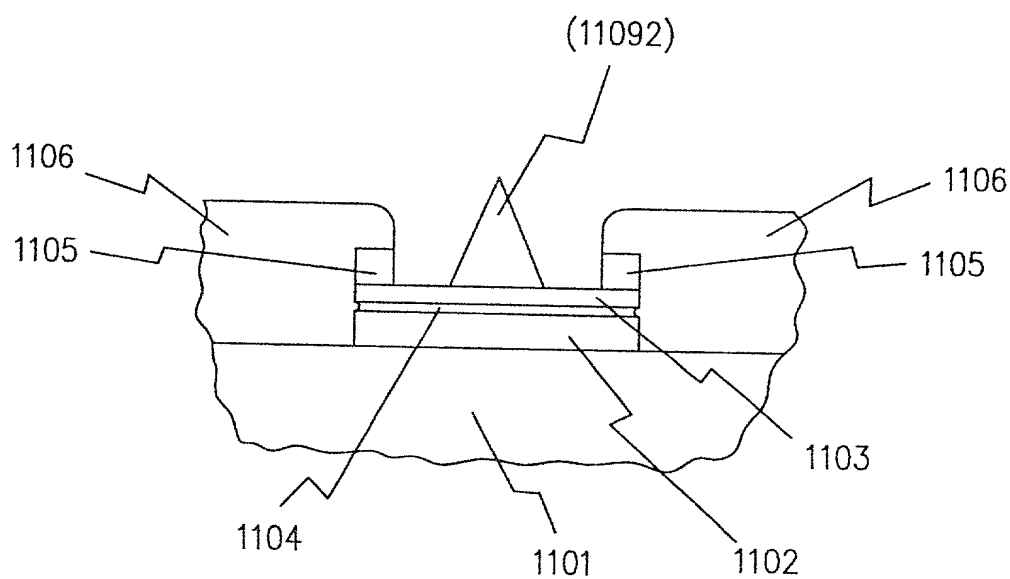


FIGURE 11d

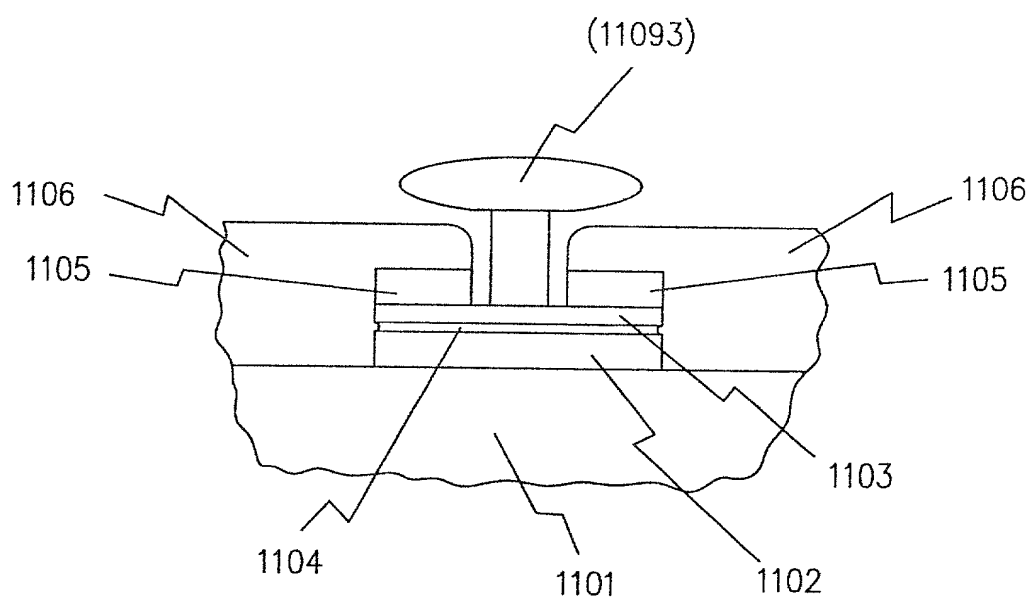


FIGURE 11e

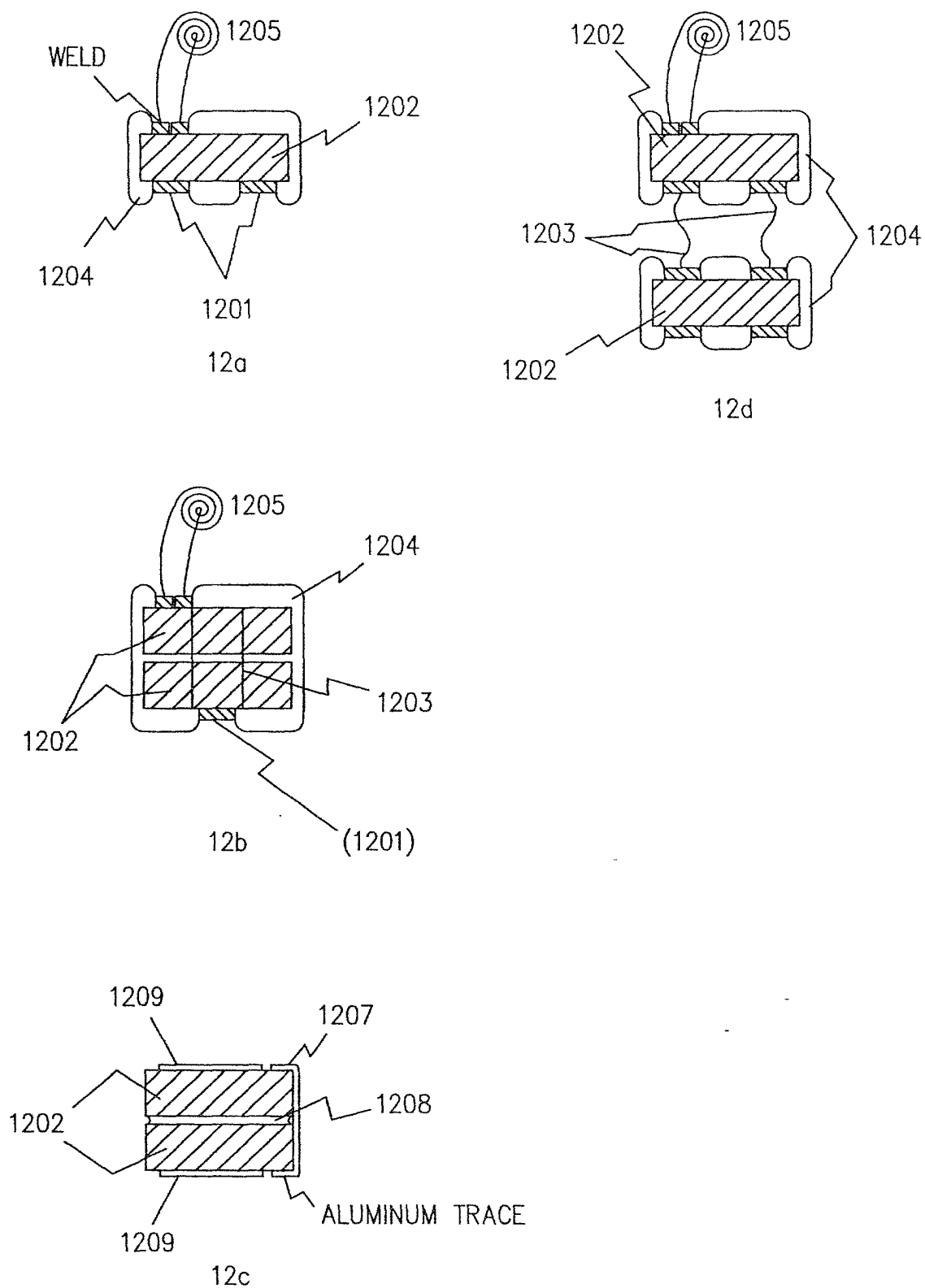


FIGURE 12
COIL AND WIRE ATTACHMENT TO SUBSTRATES AT ELECTRODE

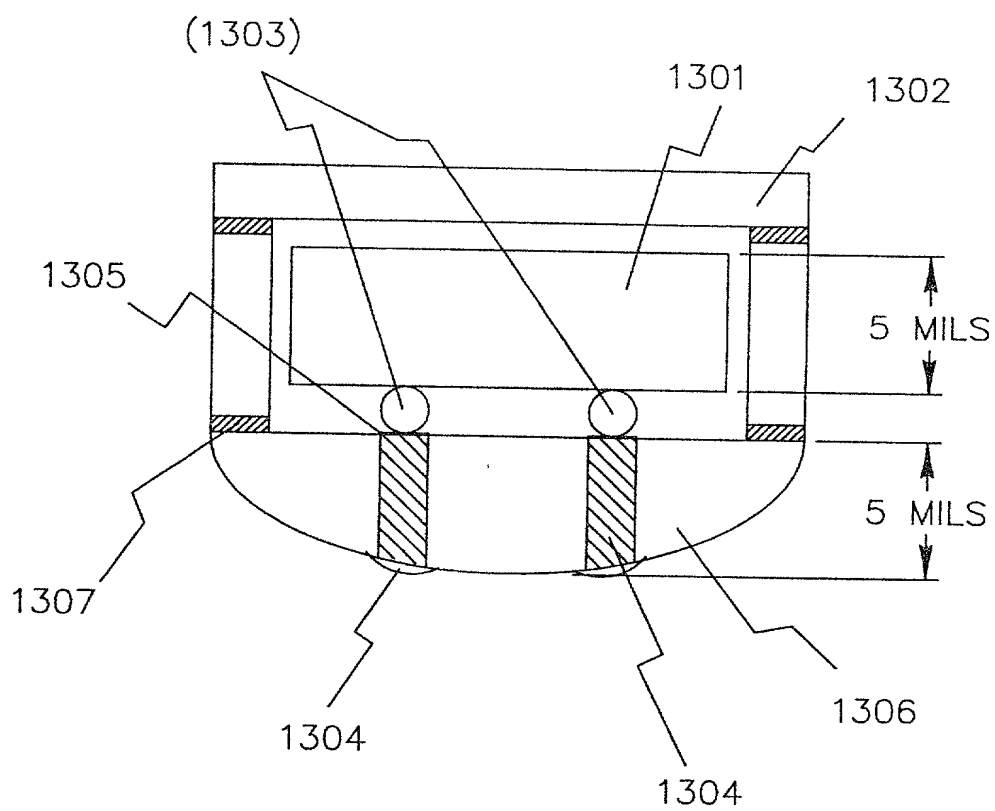


FIGURE 13
HERMETIC SEALING

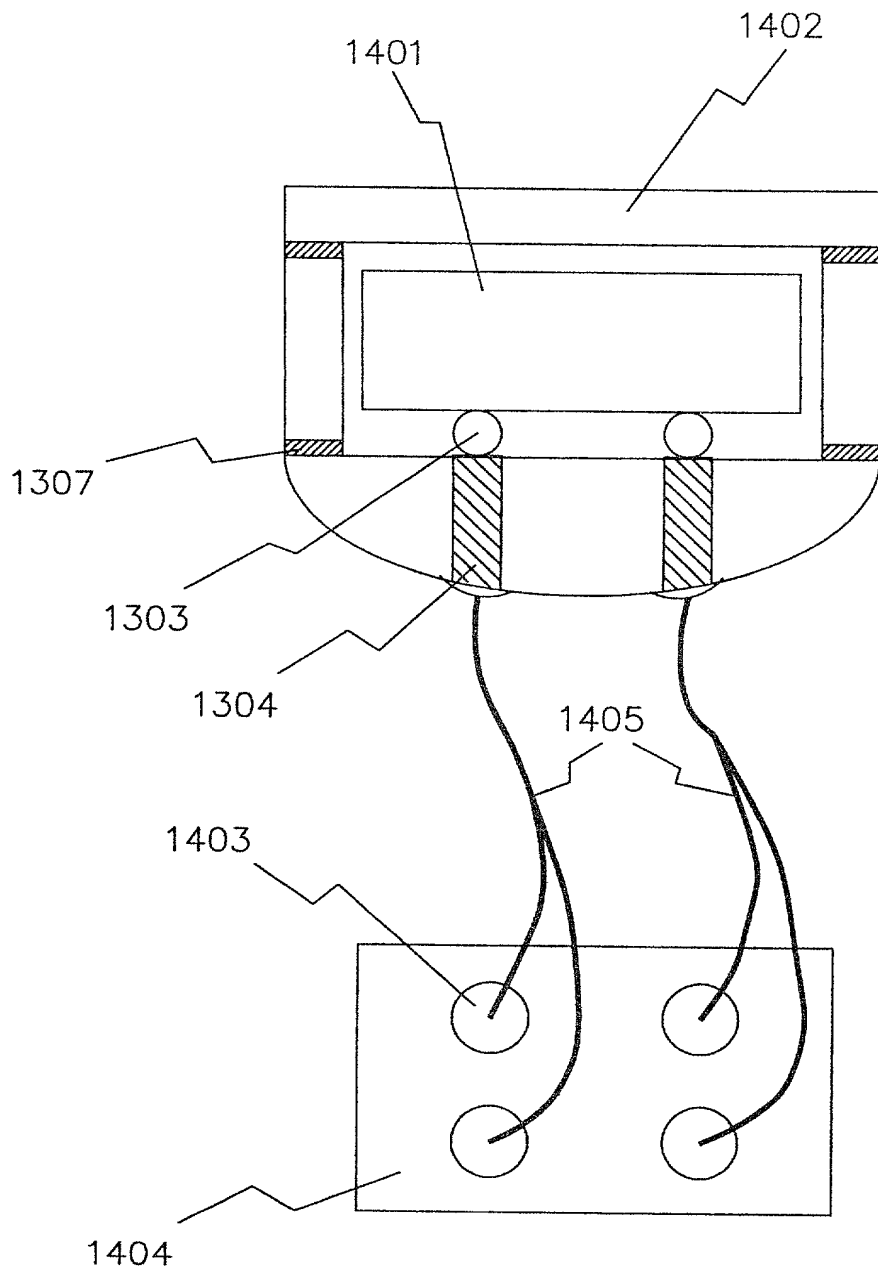


FIGURE 14

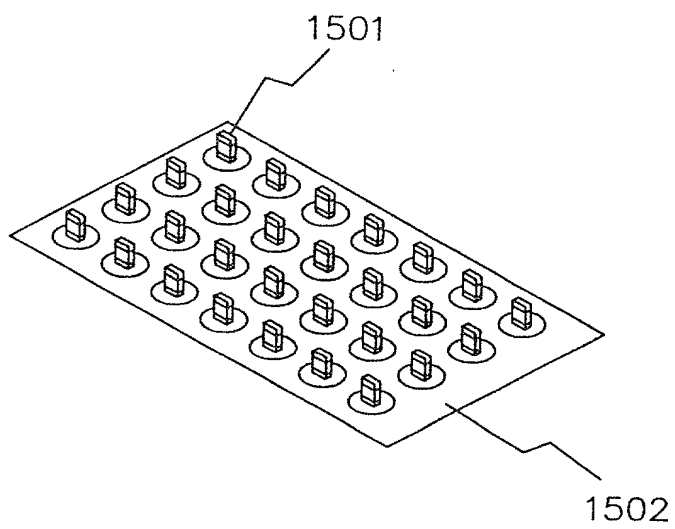


FIGURE 15

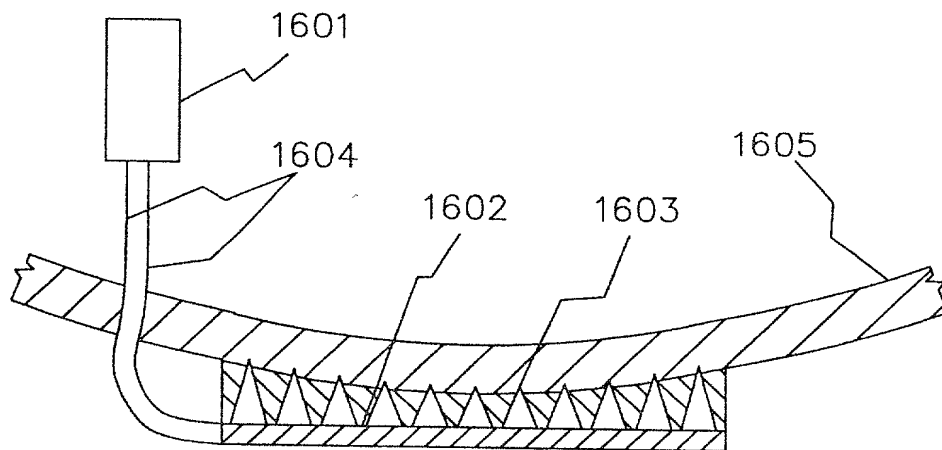


FIGURE 16a

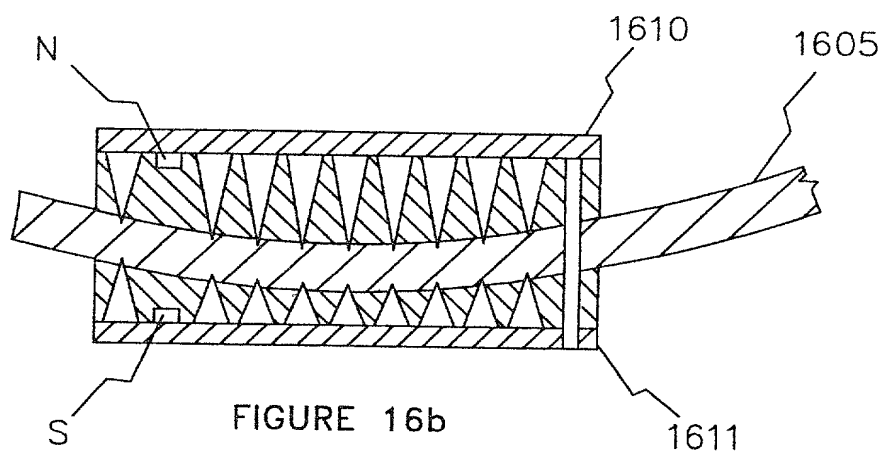


FIGURE 16b

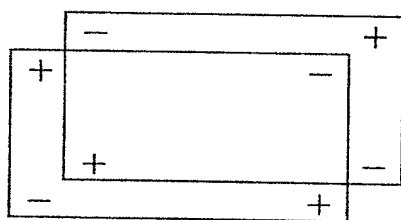


FIGURE 16c

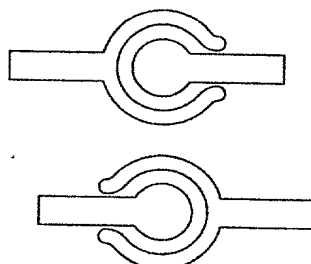


FIGURE 16d

PC PROGRAMMER
MAIN SCREEN

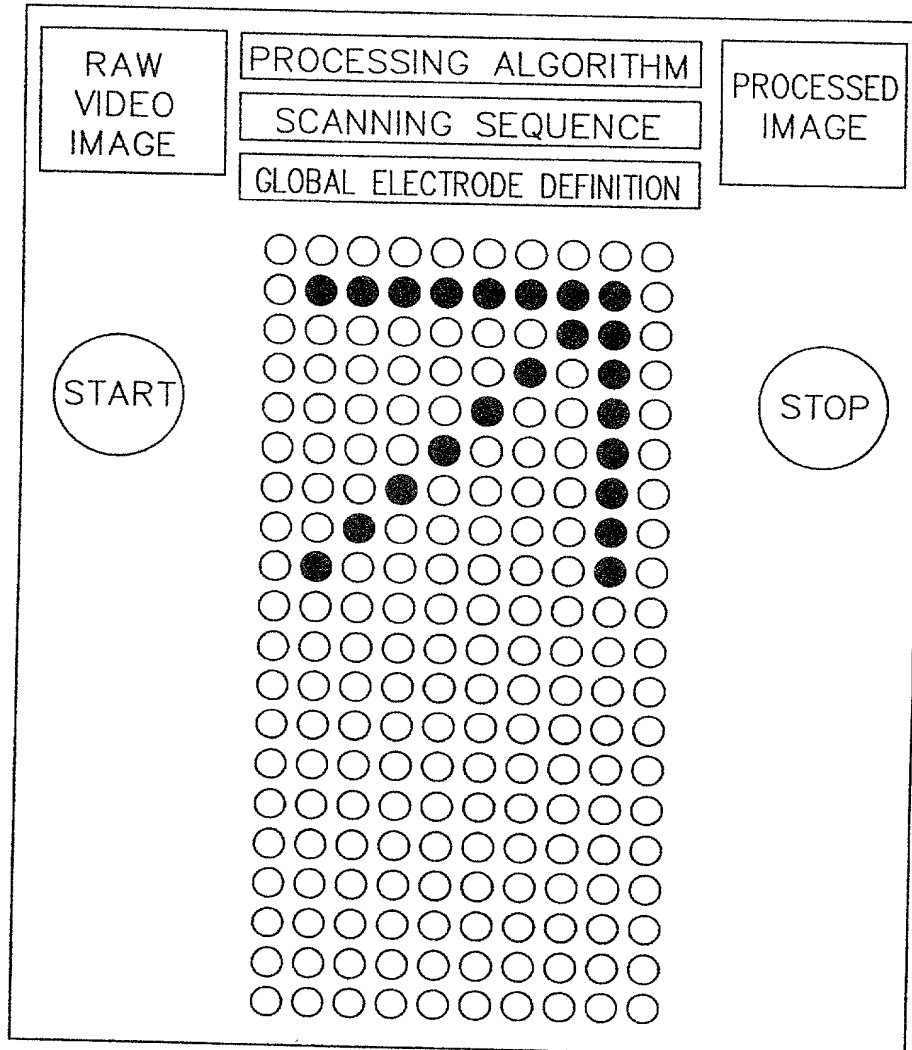
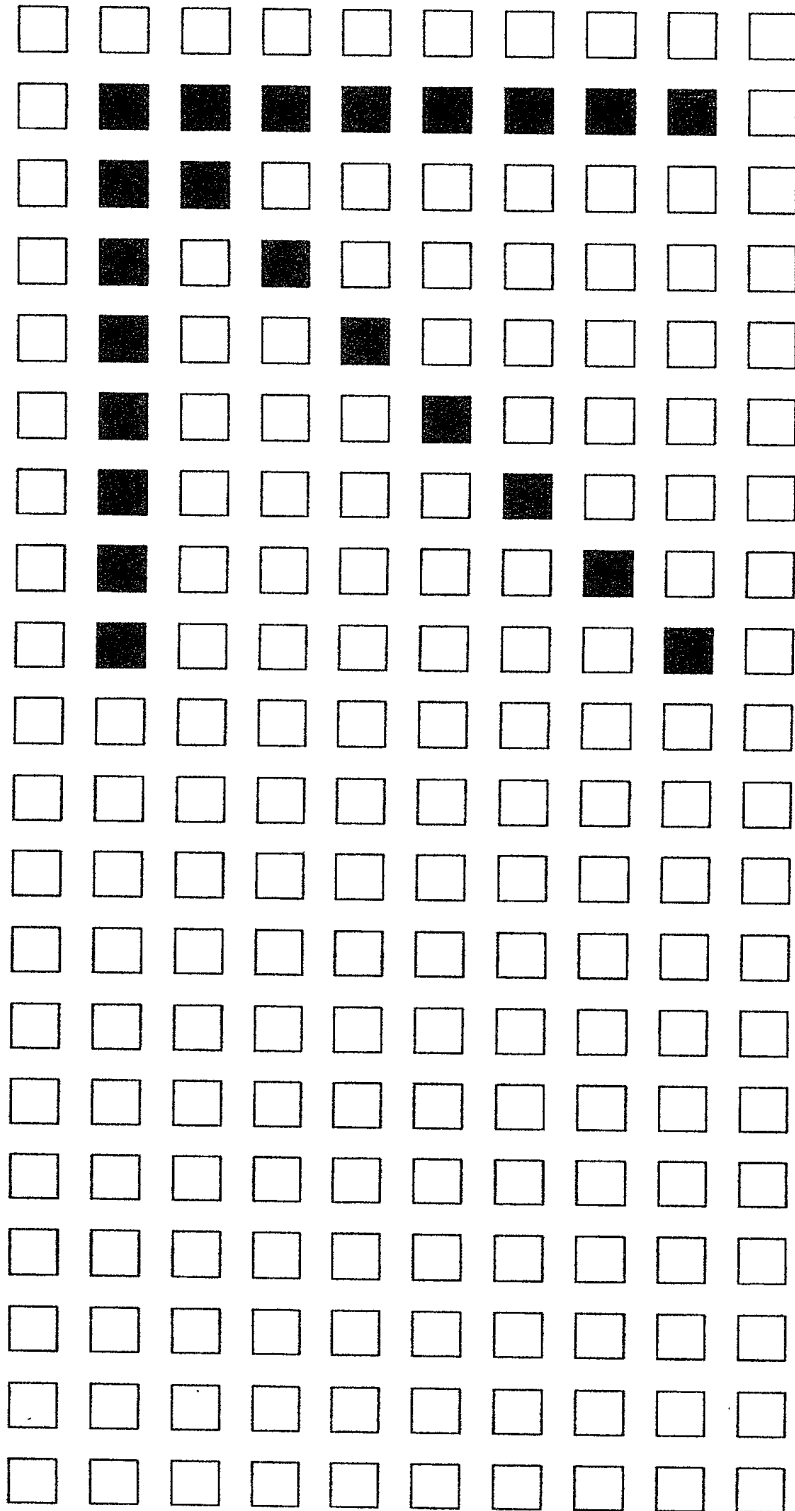


FIGURE 17a

PROCESSING ALGORITHM

PIXEL SELECTION



AVERAGE 8 SURROUNDING PIXELS

FIGURA 17b

SCANNING SEQUENCE

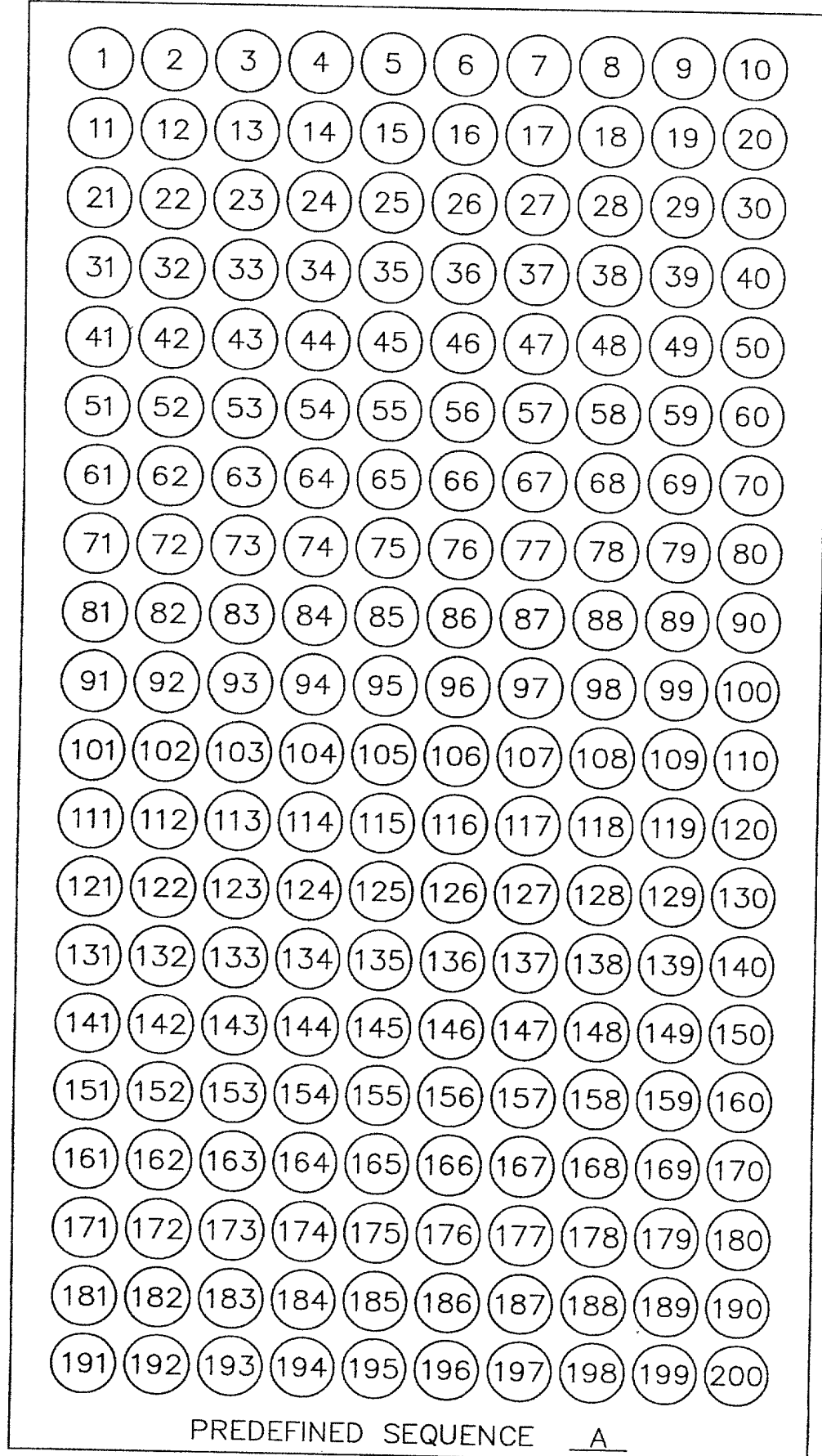


FIGURE 17c

ELECTRODE PARAMETERS

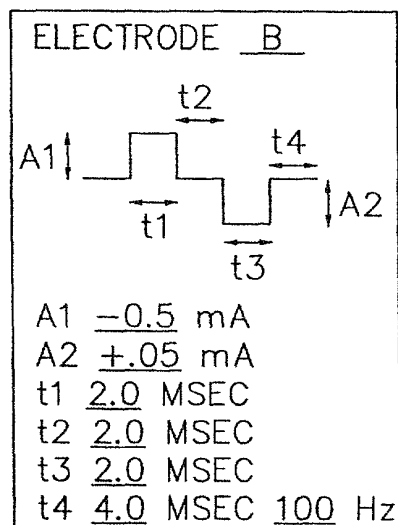


FIGURE 17d

GLOBAL ELECTRODE CONFIGURATION

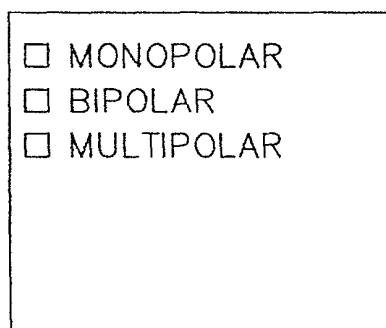


FIGURE 17e

BIPOLAR

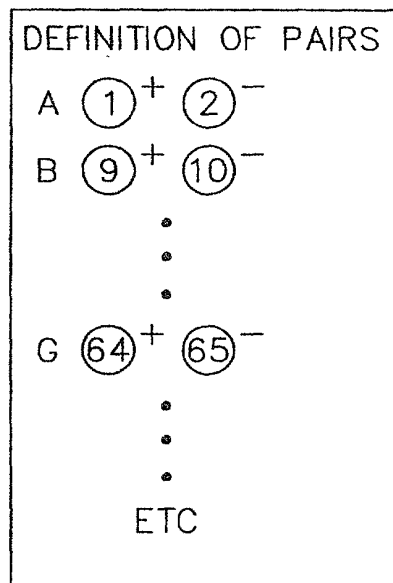


FIGURE 17f

MULTIPOLAR

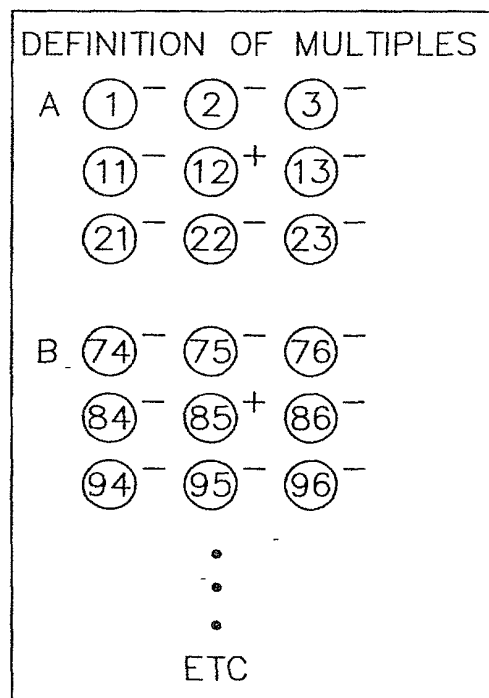


FIGURE 17g

PALM-SIZED TEST UNIT

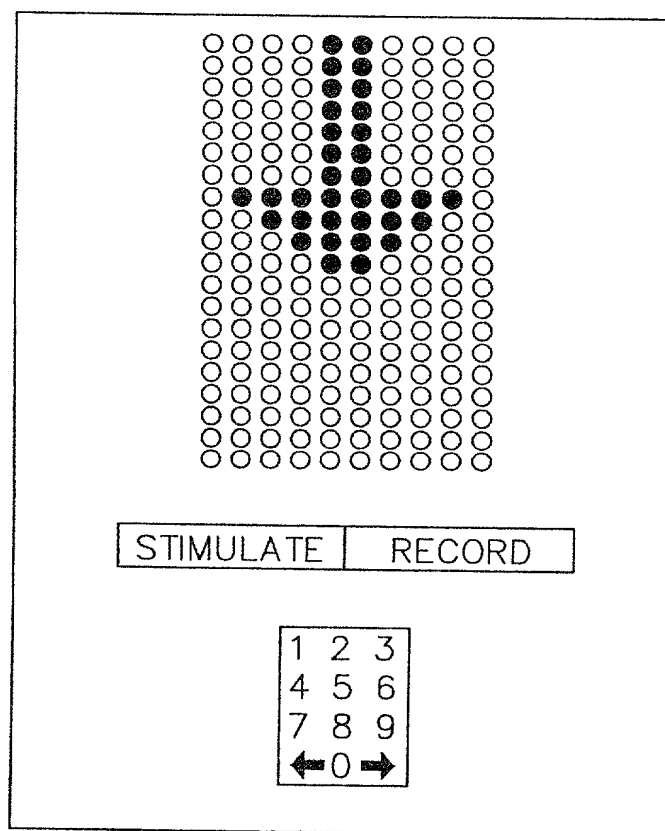


FIGURE 18a

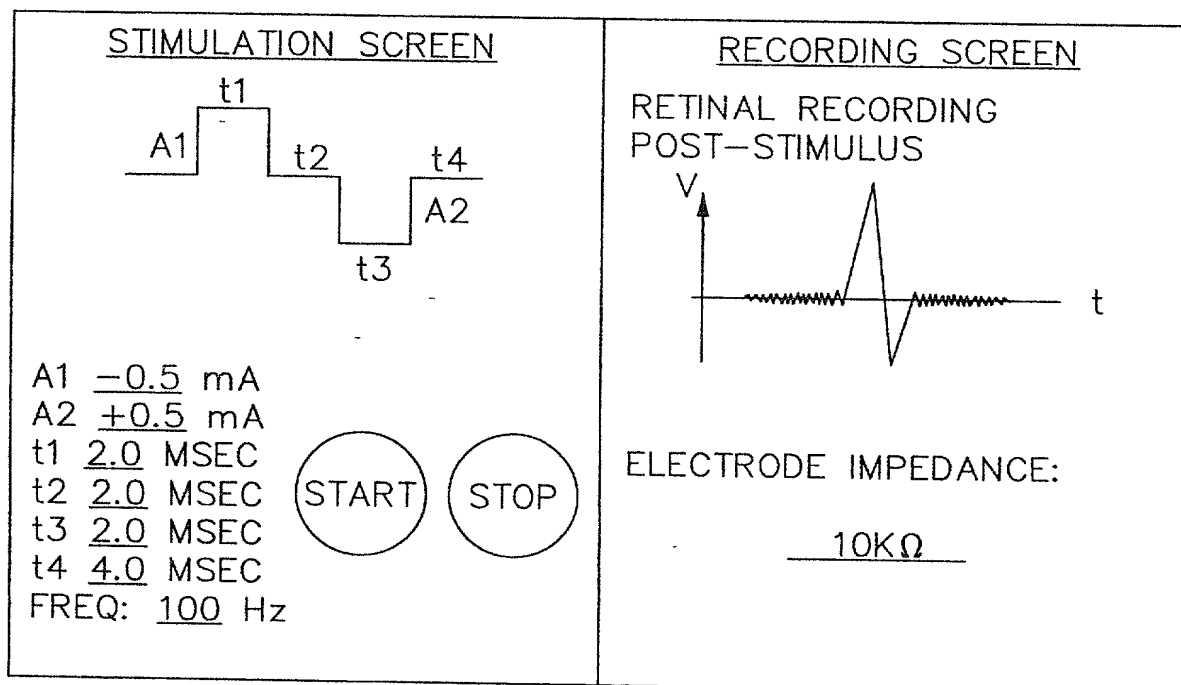


FIGURE 18b

FIGURE 18c

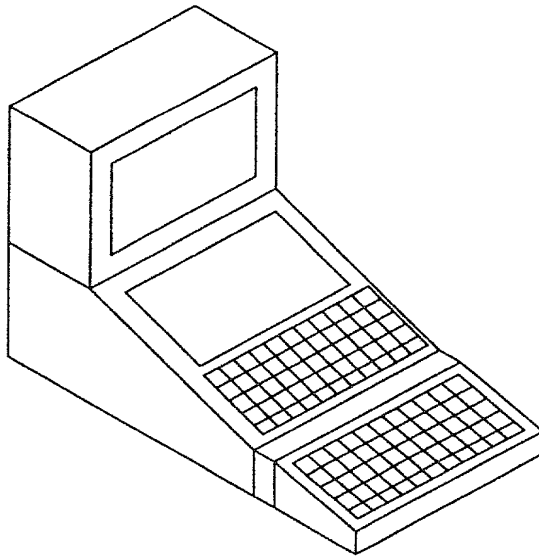


FIGURE 19a

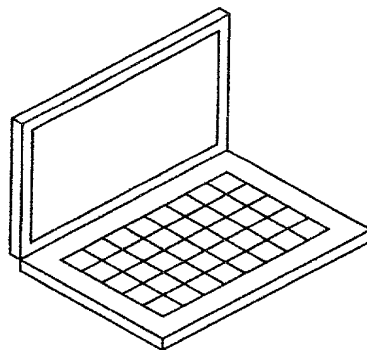


FIGURE 19b

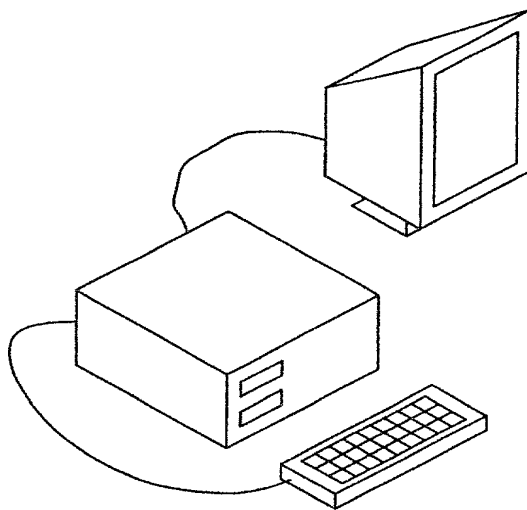


FIGURE 19c

PATIENT CONTROLLER

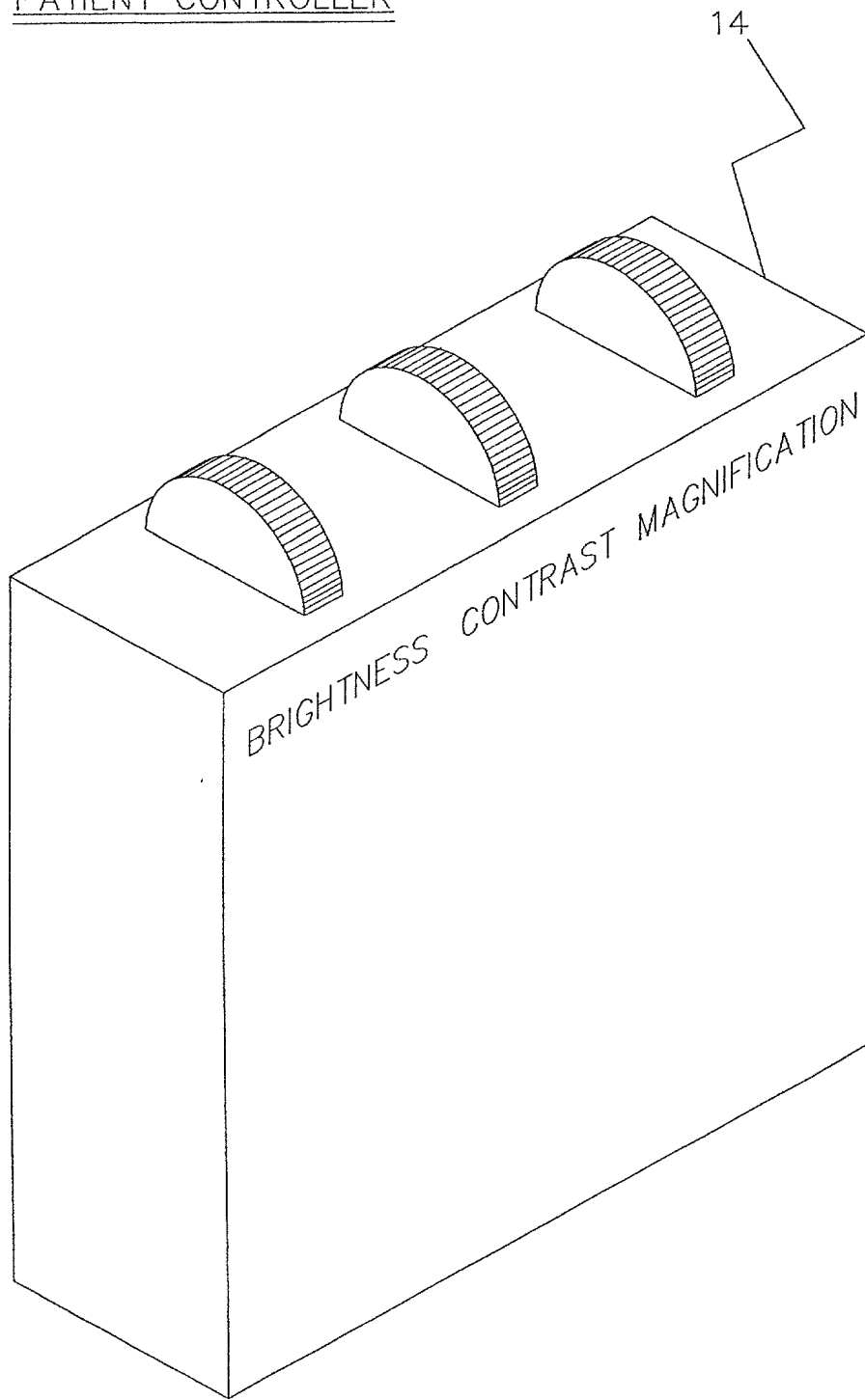


FIGURE 20